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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE JUL 21 2008

In re Applicant:	Hong Jiang et al.	§	Art Unit:	2195
Serial No.:	10/750,589	§	Examiner:	Eric Charles Wai
Filed:	December 31, 2003	§	Docket:	ITL1710US P18028
For:	Visual and Graphical Data Processing Using a Multi- Threaded Architecture	§	Assignee:	Intel Corporation

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO FINAL REJECTION**

Sir:

In response to the final rejection mailed June 11, 2008, please amend the above-referenced patent application as follows:

Date of Deposit: July 21, 2008  
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8300) on the date indicated above.  
*Cynthia L. Hayden*  
Cynthia L. Hayden